



Date : 05/01/2015

## NOTICE

All the students of SE /TE/BE electronics are hereby informed that Bridge course is scheduled from 08/01/2015 to 14/01/2015 as per the time table displayed on notice board . Attendance is compulsory.

  
HOD

(Mrs. Priti Tyagi )

**H. O. D.**

Electronics Dept.

Padmabhat  
College of Engineering  
Mumbai - 400 022

# BRIDGE TIME TABLE

TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2015)	
DEPT : ELECTRONICS ENGINEERING	
w.e.f. : 08/01/2015 TO 14/01/2015	CLASS - BE (SEM VIII)
CLASS ROOM NO-215	

TIME(HRS) DAYS	9:00 to 10:00	10:00 to 11:00	11:00 to 12:00	12:00 to 13:00	13:00 to 13:30	13:30 to 14:30	14:30 to 15:30	15:30 to 16:30
Monday	ES & RTP (/AD) R & A (/SND)	NNFS/(NZ)-III-A ANT (/KS)- 215 AVLSI (/DB)	AVLSI (/DB)	R & A (/SND)	L	ES & RTP (/AD)	NNFS/(NZ)-III-A ANT (/KS)- 215	ES & RTP (/AD)
Tuesday	R & A (/SND)	AVLSI (/DB)	NNFS/(NZ)-III-A ANT (/KS)- 215	ES & RTP (/AD)	U	NNFS/(NZ)-III-A ANT (/KS)- 215	R & A (/SND)	AVLSI (/DB)
Wednesday	AVLSI (/DB)	ES & RTP (/AD)	NNFS/(NZ)-III-A ANT (/KS)- 215	R & A (/SND)	N	AVLSI (/DB)	AVLSI (/DB)	ES & RTP (/AD)
Thursday	AVLSI (/DB)	AVLSI (/DB)	R & A (/SND)	ES & RTP (/AD)	C	NNFS/(NZ)-III-A ANT (/KS)- 215	NNFS/(NZ)-III-A ANT (/KS)- 215	R & A (/SND)
Friday	AVLSI (/DB)	ES & RTP (/AD)	R & A (/SND)	NNFS/(NZ)-III-A ANT (/KS)- 215	H	NNFS/(NZ)-III-A ANT (/KS)- 215	R & A (/SND)	ES & RTP (/AD)

SUB	CLASS/LAB TEACHER	THEORY	TOTAL
Advance VLSI Design - AVLSI	/DB- Mrs. Deepali Bhosle	09	09
Embedded Systems and Real Time Programming - ES & RTP	/AD - Mrs. Anagha D.	09	09
Robotics and Automation - R & A	/SND- Mrs. Shuhada Deshpande	09	09
Advanced Networking Technologies-ANT	/KS - Mrs. Khushboo Singh	04	04
Neural Networks & Fuzzy Systems - NNFS	/NZ - Mrs. Nilima Zade	09	09

FH-15 BE

*[Signature]*

Mrs. Priya G / Mrs. Radha W  
(Academic Committee Member)

*[Signature]*

Mrs. Medha Kulkarni  
(Academic Committee Convener)

*[Signature]*

Mrs. Priti Tyagi  
(HOD ELEX)

*[Signature]*

Dr. Rajendra Sawant  
(PRINCIPAL)


# BRIDGE TIME TABLE


TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2015)		CLASS - TE (SEM VI)		CLASS ROOM NO-214	
DEPT : ELECTRONICS ENGINEERING					
w.e.f : 08/01/2015 TO 14/01/2015					

TIME(HRS) DAYS	9:00 to 10:00	10:00 to 11:00	11:00 to 12:00	12:00 to 13:00	13:00 to 13:30	13:30 to 14:30	14:30 to 15:30	15:30 to 16:30
Monday	B-VLSI (NN)	DSPP (/PT)	DSPP (/PT)	PE-I (/JP)	L	PE-I (/JP)	CO (/RW)	B-VLSI (NN)
Tuesday	DSPP (/PT)	PE-I (/JP)	B-VLSI (NN)	DSPP (/PT)	U	B-VLSI (NN)	AIS (/KS)	CO (/RW)
Wednesday	AIS (/JP)	MIT (SB)	B-VLSI (NN)	AIS (/JP)	N	B-VLSI (NN)	CO (/RW)	PE-I (/JP)
Thursday	DSPP (/PT)	MIT (SB)	CO (/RW)	AIS (/JP)	C	B-VLSI (NN)	PE-I (/JP)	MIT (SB)
Friday	DSPP (/PT)	AIS (/KS)	CO (/RW)	AIS (/JP)	H	CO (/RW)	PE-I (/JP)	MIT (SB)


SUB	CLASS/LAB TEACHER	THEORY	TOTAL
Digital Signal Processing and Process - DSPP	/PT - Mrs. Priti Tyagi	06	06
Computer Organization - CO	/RW - Mrs. Radha Wanode	06	06
Power Electronics I - PE I	/JP - Mrs. Jayshree Pawar	06	06
Basic VLSI Design - B VLSI	NN - Mr. Nilesh Nagrale	07	07
Advance Instrumentation System- AIS	/JP - Mrs. Jayshree Pawar	05	05
Modern Information Technology for Management - MITM	SB- Mr. Satvinder Singh Bedi	04	04

FH-15-TE

  
Mrs. Priya G / Mrs. Radha W  
(Academic Committee Member)

  
Mrs. Medha Kulkarni  
(Academic Committee Convener)

  
Mrs. Priti Tyagi  
(HOD ELEX)

  
Dr. Rajendra Sawant  
(PRINCIPAL)

TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2015)

DEPT : ELECTRONICS ENGINEERING

w.e.f. : 08/01/2015 TO 14/01/2015


CLASS - SE (SEM IV)

CLASS ROOM NO-213


TIME(HRS) DAYS	9:00 to 10:00	10:00 to 11:00	11:00 to 12:00	12:00 to 13:00	13:00 to 13:30	13:30 to 14:30	14:30 to 15:30	15:30 to 16:30
Monday	MATHS - IV (/AK)	DEC (/RW)	FCE (/LG)	MPP (/PG)	L	MATHS - IV (/AK)	PCS (PB)	PCS (PB)
Tuesday	PCS (PB)	MPP (/PG)	MPP (/PG)	DEC (/RW)	U	MATHS - IV (/AK)	FCE (/LG)	EM (/NZ)
Wednesday	MATHS - IV (/AK)	MATHS - IV (/AK)	PCS (PB)	DEC (/RW)	N	FCE (/LG)	MATHS - IV (/AK)	MPP (/PG)
Thursday	DEC (/RW)	EM (/NZ)	MATHS - IV (/AK)	PCS (PB)	C	FCE (/LG)	MPP (/PG)	DEC (/RW)
Friday	MPP (/PG)	FCE (/LG)	DEC (/RW)	MATHS - IV (/AK)	H	PCS (PB)	EM (/NZ)	FCE (/LG)

SUB	CLASS/LAB TEACHER	THEORY	TOTAL
Fundamental of Communication - FCE	/LG - Mrs. Leena Govekar	06	06
Mathematics IV - MATHS IV	/AK - Mrs. Aarti Kurkure	06	06
Electrical Machines - EM	/NZ - Mrs. Nilima Zade	05	05
Microprocessor and Peripherals - MPP	/PG - Mrs. Priya Gupta	06	06
Discrete Electronic Circuit - DEC	/RW - Mrs. Radha Wanode	06	06
Principle of Control System - PCS	PB - Mr. Pramod Bhavarthe	06	06
TOTAL			

FH-15  
SE

  
Mrs. Priya Gupta  
(Academic Committee Member)

  
Mrs. Medha Kulkarni  
(Academic Committee Convener)

  
Mrs. Priti Tyagi  
(HOD ELEX)

  
Dr. Rajendra Sawant  
(PRINCIPAL)